CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI (RAJ.)

F.No. 4/2-3(2)/2016-Estt.I Date: 21/06/2021

Advt. No. PF-04/2021

"Online-Interview"

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI), Pilani is a premier research Institute in the field of Electronics, set up under the Council of Scientific & Industrial Research (CSIR). The Institute is looking for qualified candidates for the following purely temporary positions of Project Staff for various Projects going on in the Institute in Semiconductor Devices Design Group (SDDG), Vacuum Electron Devices Design Group (VEDDG), Societal Electronics Group (SEG), Semiconductor Device Fabrication Group (SDFG), and Electronics System Area (ESA) CEERI, Jaipur Centre.

Online applications are invited from candidates for Engagement of project Staff.

Details of Vacancies & Eligibility

Post Code	***Project No.***	***Area/ Group***	***Name & Number of Vacancy***	***Qualifications***	***Age***	***Consolidated Emoluments per month (fixed)***
0501	GAP-6517	SDDG	**Junior Research Fellow (JRF)** 02	M.Sc. (Electronics/NanoScience/ NanoElectronics/Physics/Applied Physics/Applied Electronics/E&I OR BE / B.Tech / ME/M.Tech in (EEE / Electronics/ EE/ECE/ Electronics & Instrumentation/VLSI/ Microelectronics/Nanotechnology/ MEMS) or equivalent; selected through a process described through any one of the following: a) Scholars who are selected through National Eligibility Tests-CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. b) The selection process through National Level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER etc.	30 Years	Rs. 31000/- + HRA as per rule
0502	GAP-3331	VEDDG	**Senior Project Fellow (SPF)** 01	M.E./M.Tech (Electronics & Communications / Microwave / RF and Microwave / microwave and RADAR) Or M.Sc. (Electronics) / B.E. / B.Tech (Electronics & Communications / Telecommunication / Electronics & Tele-communications) with tow years experience in relevant field.	35 Years	Rs. 35000/- + HRA as per rule

				Desired Qualification and Experience: (1) Experience in using 3D electromagnetic simulation tools (i.e. CST design studio, Ansys HFSS, Magic etc.) and MATLAB. (2) Scholars who are selected through National Eligibility Tests – like CSIR-NET and GATE.		
0503	GAP-3331	VEDDG	**Senior Project Fellow (SPF)** 01	M.E. / M.Tech (Instrumentation) Or M.Sc. (Electronics) / B.E. / B.Tech (Electronics & Communications / Electrical and Electronics / Instrumentation) with two year experience in relevant field.	35 Years	Rs. 35000/- + HRA as per rule
0504	MLP-0119	VEDDG	**Project Associate – I** 01	B.E. / B. Tech (Agriculture Engineering).	35 Years	*Rs. 31,000/- + HRA as per rule **Rs.25,000/ +HRA as per rule
0505	MLP-0119	VEDDG	**Project Associate – II** 01	B.E. / B.Tech. (Food Technology) with 02 years of relevant experience.	35 Years	#Rs. 35,000/- + HRA as per rule ##Rs. 28,000/- + HRA as per rule
0506	MLP-0119	VEDDG	**Project Associate – II** 01	B.E./ B.Tech. (Biomedical Engineering / Biotechnology) with 02 years of relevant experience.	35 Years	#Rs. 35,000/- + HRA as per rule ##Rs. 28,000/- + HRA as per rule
0507	HCP-0026T2.4	SEG	**Project Associate – I** 01	B.E. / B.Tech. / M.Sc. (Electronics /Instrumentation/Instrumentation and Control system / Power Electronics / EEE) or equivalent.	35 Years	*Rs. 31,000/- + HRA as per rule **Rs. 25,000/- + HRA as per rule
0508	HCP-0026T2.4	SEG	**Project Associate – II** 01	B.E. / B.Tech. / M.Sc. / (Electronics / Instrumentation / Instrumentation and Control system / Power Electronics / EEE / Mechanical Engineering / Embedded System / Robotics / Mechatronics) or equivalent. With 2 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisations and Scientific activities and services.	35 Years	#Rs. 35,000/- + HRA as per rule ##Rs. 28,000/- + HRA as per rule

0509	GAP-6223	SEG	**Junior Research Fellow (JRF)** 01	M.Sc. (Physics/Electronics)/B.E. / B.Tech. / M.E. / M.Tech. (ECE / Electronics & Instrumentation / EEE / Electronics / Nanotechnology / Embedded Systems / VLSI & Embedded) or equivalent; selected through a process described through any one of the following: a) Scholars who are selected through National Eligibility Tests-CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. b) The selection process through National Level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER etc.	30 Years	Rs. 31,000/- + HRA as per rule
0510	GAP-6518	SDFG	**Junior Research Fellow (JRF)** 01	M.Sc. in (Electronics/ Physics/ Applied Physics / Applied Electronics / E&I) OR BE/B.Tech. M.E./M.Tech. in EEE / Electronics / ECE / Electronics & Instrumentation or equivalent. Selected through a process described through any one of the following: a) Scholars who are selected through National Eligibility Tests – CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. b) The selection process through national Level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER etc.	30 Years	Rs. 31,000/- + HRA as per rule
0511	GAP-6518	SDFG	**Senior Research Fellow (SRF)** 01	M.Sc. in (Electronics/ Physics/ Applied Physics / Applied Electronics / E&I) OR BE/B.Tech. M.E./M.Tech. in EEE / Electronics/ ECE / Electronics & Instrumentation or equivalent. Selected through a process described through any one of the following: a) Scholars who are selected through National Eligibility Tests – CSIR-UGC NET	32 Years	Rs. 35,000/- + HRA as per rule

				including lectureship (Assistant Professorship) and GATE. b) The selection process through national Level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER etc. **With two years of research experience**		
0512	GAP-3139	ESA	**Junior Research Fellow (JRF)** 01	M.Sc. in Electronics OR B.E. / B.Tech in Elect & Com, Electrical & Electronics, Elec & Inst., Comp. Sci, Infor & Tech or equivalent. Selected through a process described through any one of the following: a) Scholars who are selected through National Eligibility Tests – CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. b) The selection process through national Level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER etc.	30 Years	Rs. 31,000/- + HRA as per rule
0513	GAP-3332	VEDDG	**Project Associate – I** 01	B.E. / B.Tech. / M.Sc. (Electrical / Electronic Engineering / Electronics & Communication / Instrumentation / Physics) or equivalent. **<u>Desirable:</u>** Candidates having experience in design and development works related to Plasma Assisted Devices and Technologies, would be given preferences.	35 Years	*Rs. 31,000/- + HRA as per rule **Rs. 25,000/- + HRA as per rule
0514	GAP-3322	VEDDG	**Project Assistant** 01	3 years Diploma in Electronics	50 Years	Rs. 20,000/- + HRA as per rule
0515	GAP – 3239	SEG	**Senior Project Fellow (SPF)** 01	ME/M.Tech in Material Science, Nanoscience and technology, Nanotechnology or equivalent	40 Years	Rs. 28000/- + HRA as per rule

*** (i) (Rs. 31,000/- + HRA) to Scholars who are selected through –**

(a) National Eligibility Test – CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or

(b) A selection process through national level examinations conducted by Central Government Departments and their Agencies and Institutions.

**** (ii) (Rs. 25,000/- + HRA) for others who do not fall under (i) above.**

#(i) (Rs. 35,000/- + HRA) to Scholars who are selected through –

(a) National Eligibility Test – CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or

(b) A selection process through national level examinations conducted by Central Government Departments and their Agencies and Institutions.

##(ii) (Rs. 28,000/- + HRA) for others who do not fall under (i) above.

Note: If any candidate wishes to apply against two or more post codes, then such candidates have to apply separately.

Interested candidates fulfilling the above qualifications must apply through online portal available on CEERI website. The shortlisted candidates will be informed through E-mail/Institute website along with the date and time for Interview. Candidates are requested to frequently browse the Institute website. The eligible candidates must appear for interviews through "Microsoft Teams" (MS Teams). Candidates will be allowed as a Guest in Ms Team, for which the link will be sent to their registered email. The selected candidates are expected to join immediately upon selection and bring with them all ORIGINAL TESTIMONIALS including marksheets and certificates and also a passport size photograph at the time of joining, failing which their engagement can be withdrawn.

The date of applying applications is from 22/06/2021 11:00 AM to 28/06/2021 till 05:00 PM. Application received after last date will not be entertained.

For recruitment queries:	**For technical queries:**
Sh. Pankaj Goswami **Administrative Officer** **CSIR-CEERI, Pilani** **Email: recruitment@ceeri.res.in** **Phone: 01596-252275**	**Sh. Arvind Kumar Khandelwal** **Network Management Cell** **CSIR-CEERI, Pilani** **Email: arvindkumar@ceeri.res.in** **Phone: 01596-252282**

Other Conditions:

1. Candidates who have already served CSIR-CEERI, Pilani or any other lab / Institute of CSIR as Project Staff such as Project Assistant / Project Fellow / JRF (in contract R&D Projects) / SRF (in contract R&D Projects) / Research Associate etc. for a total period of 5 years or more are not eligible for these engagements. The candidates who have served for a period less than 5 years will have tenure up-to remaining period till completion of five years.

2. Tenure of engagement: Six months initially or co-terminus with the present project or till such time the job to be performed by Project Staff in the project exists, whichever is earlier. The tenure may be extended in steps of six months for the duration of Project based on satisfactory performance. The total tenure as Project Staff shall not exceed 5 years in any case. The total tenure of five years shall be calculated as per periods spent on one Project and/ or any other Lab. / Instt. of CSIR as RA, project Assistant Level I/II/III etc. or as any other designation of equal status.

3. **Reservation:** As regards reservation, if all things are equal, SC/ST/OBC/PH/EWS candidates may be given preference over General candidates so as to ensure their representation.

4. The engagement of Project Assistant – II/III, Project Associate – I/II, JRF etc. will be made on behalf of the Sponsor of the Projects. These are not CSIR-CEERI, Pilani/CSIR posts and will not confer any right on the incumbent to claim, implicit or explicit, on any post in CSIR-CEERI, Pilani/CSIR.

5. Age as on the last date of application will be taken into consideration. Relaxation in age limit for SC/ST/OBC/PH and women shall be 5 years.

6. Director, CSIR-CEERI, Pilani reserve the right to cancel or withdraw the Offer of engagement in case of any discrepancy found, in the candidature of any empanelled candidate at any stage.

7. The number of positions may be increased or decreased as per the requirements. The candidate must hold all the eligibility criteria fulfilled on or before the date of interview.

8. Other terms & conditions will be governed as per guidelines issued by the funding agency / CSIR/ CSIR-CEERI, Pilani for the engagement of above Project Staff as amended from time to time.

S/d
Administrative Officer